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The second edition of Razavi ' s Fundamentals of Microelectronics retains its hallmark emphasis on analysis by inspection and building students ' design intuition. It incorporates a host of new pedagogical features that make the book easy to teach and learn from, including application sidebars; self-check problems with answers; simulation problems with SPICE and MULTISIM; and an expanded problem set that is organized by degree of difficulty and clearly associated with specific chapter sections.

Fundamentals of Microelectronics: Razavi, Behzad ...

Professor Razavi is an IEEE Distinguished Lecturer, a Fellow of IEEE, and the author of a number of books, including Principles of Data Conversion System Design, RF Microelectronics (translated to Chinese and Japanese), Design of Analog CMOS Integrated Circuits (translated to Chinese and Japanese), Design of Integrated Circuits for Optical communications, and Fundamentals of Microelectronics. he is also the editor of Monolithic Phase-Locked Loops and Clock recovery circuits and Phase-Locking ...

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Chinese, Japanese, and Korean), Design of Analog CMOS Integrated Circuits (translated to Chinese, Japanese, and Korean), Design of Integrated Circuits for Optical Communications, and Fundamentals of Microelectronics (translated to Korean and Portuguese).

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Fundamentals of Microelectronics Behzad Razavi Fundamentals of Microelectronics, 2nd Edition is designed to build a strong foundation in both design and analysis of electronic circuits this text offers conceptual understanding and mastery of the material by using modern examples to motivate and prepare readers for advanced courses and their careers.

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Fundamentals of Microelectronics. To succeed in the practice of microelectronics in industry, students must develop the ability to think intuitively about circuits. They need to move beyond simply plugging and chugging numbers in equations and be prepared to face real design trade-offs.

~~Fundamentals of Microelectronics by Behzad Razavi~~

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~~Editions of Fundamentals of Microelectronics by Behzad Razavi~~

BR Wiley/Razavi/Fundamentals of Microelectronics [Razavi.cls v. 2006] June 30, 2007 at 13:42
1 (1) 1 Introduction to Microelectronics Over the past five decades, microelectronics has revolutionized our lives. While beyond the realm of possibility a few decades ago, cellphones, digital cameras, laptop computers, and many other

~~Introduction to Microelectronics~~

Behzad Razavi (Persian:) is an Iranian-American professor and researcher of electrical and electronic engineering. Noted for his research in communications circuitry, Razavi is the director of the Communication Circuits Laboratory at the University of California Los Angeles. He is a Fellow and a distinguished lecturer for the Institute of Electrical and Electronics Engineers.

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Fundamentals of Microelectronics, 2nd Edition is designed to build a strong foundation in both design and analysis of electronic circuits this text offers conceptual understanding and mastery of the material by using modern examples to motivate and prepare readers for advanced courses and their careers. The book's unique problem-solving framework enables readers to deconstruct complex problems into components that they are familiar with which builds the confidence and intuitive skills needed for success.

By helping students develop an intuitive understanding of the subject, Microelectronics teaches them to think like engineers. The second edition of Razavi ' s Microelectronics

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retains its hallmark emphasis on analysis by inspection and building students' design intuition, and it incorporates a host of new pedagogical features that make it easier to teach and learn from, including: application sidebars, self-check problems with answers, simulation problems with SPICE and MULTISIM, and an expanded problem set that is organized by degree of difficulty and more clearly associated with specific chapter sections.

This modern, pedagogic textbook from leading author Behzad Razavi provides a comprehensive and rigorous introduction to CMOS PLL design, featuring intuitive presentation of theoretical concepts, extensive circuit simulations, over 200 worked examples, and 250 end-of-chapter problems. The perfect text for senior undergraduate and graduate students.

The Acclaimed RF Microelectronics Best-Seller, Expanded and Updated for the Newest Architectures, Circuits, and Devices Wireless communication has become almost as ubiquitous as electricity, but RF design continues to challenge engineers and researchers. In the 15 years since the first edition of this classic text, the demand for higher performance has led to an explosive growth of RF design techniques. In RF Microelectronics, Second Edition, Behzad Razavi systematically teaches the fundamentals as well as the state-of-the-art developments in the analysis and design of RF circuits and transceivers. Razavi has written the second edition to reflect today's RF microelectronics, covering key topics in far greater detail. At nearly three times the length of the first edition, the second edition is an indispensable tome for both students and practicing engineers. With his lucid prose, Razavi now Offers a stronger tutorial focus along with hundreds of examples and problems Teaches design as well as analysis with the aid of step-by-step design procedures and a chapter dedicated to the design of a dual-band WiFi transceiver Describes new design paradigms and analysis techniques for circuits such as low-noise amplifiers, mixers, oscillators, and frequency dividers This edition's extensive coverage includes brand new chapters on mixers, passive devices, integer-N synthesizers, and fractional-N synthesizers. Razavi's teachings culminate in a new chapter that begins with WiFi's radio specifications and, step by step, designs the transceiver at the transistor level. Coverage includes Core RF principles, including noise and nonlinearity, with ties to analog design, microwave theory, and communication systems An intuitive treatment of modulation theory and wireless standards from the standpoint of the RF IC designer Transceiver architectures such as heterodyne, sliding-IF, directconversion, image-reject, and low-IF topologies. Low-noise amplifiers, including cascode common-gate and commonsource topologies, noise-cancelling schemes, and reactance-cancelling configurations Passive and active mixers, including their gain and noise analysis and new mixer topologies Voltage-controlled oscillators, phase noise mechanisms, and various VCO topologies dealing with noise-power-tuning trade-offs All-new coverage of passive devices, such as integrated inductors, MOS varactors, and transformers A chapter on the analysis and design of phase-locked loops with emphasis on low phase noise and low spur levels Two chapters on integer-N and fractional-N synthesizers, including the design of frequency dividers Power amplifier principles and circuit topologies along with transmitter architectures, such as polar modulation and outphasing

By helping students develop an intuitive understanding of the subject, Fundamentals of Microelectronics teaches them to think like engineers. The second edition of Razavi's Fundamentals of Microelectronics retains its hallmark emphasis on analysis by inspection and building students' design intuition, and it incorporates a host of new pedagogical features that make it easier to teach and learn from, including: application sidebars, self-check problems with answers, simulation problems with SPICE and MULTISIM, and an

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expanded problem set that is organized by degree of difficulty and more clearly associated with specific chapter sections.

Featuring an extensive 40 page tutorial introduction, this carefully compiled anthology of 65 of the most important papers on phase-locked loops and clock recovery circuits brings you comprehensive coverage of the field—all in one self-contained volume. You'll gain an understanding of the analysis, design, simulation, and implementation of phase-locked loops and clock recovery circuits in CMOS and bipolar technologies along with valuable insights into the issues and trade-offs associated with phase locked systems for high speed, low power, and low noise.

"One salient feature of this book is its synthesis- or design-oriented approach. Rather than pulling a circuit out of a bag and trying to analyze it, I set the stage by stating a problem that we face in real life (e.g., how to design a cellphone charger). I then attempt to arrive at a solution using basic principles, thus presenting both failures and successes in the process. When we do arrive at the final solution, the student has seen the exact role of each device as well as the logical thought sequence behind synthesizing the circuit. Another essential component of this book is "analysis by inspection." This "mentality" is created in two steps. First, the behavior of elementary building blocks is formulated using a "verbal" description of each analytical result (e.g., "looking into the emitter, we see 1/gm."). Second, larger circuits are decomposed and "mapped" to the elementary blocks to avoid the need for writing KVLs and KCLs. This approach both imparts a great deal of intuition and simplifies the analysis of large circuits"--

With the exponential growth of the number of Internet nodes, the volume of the data transported on the backbone has increased with the same trend. The load of the global Internet backbone will soon increase to tens of terabits per second. This indicates that the backbone bandwidth requirements will increase by a factor of 50 to 100 every seven years. Transportation of such high volumes of data requires suitable media with low loss and high bandwidth. Among the available transmission media, optical fibers achieve the best performance in terms of loss and bandwidth. High-speed data can be transported over hundreds of kilometers of single-mode fiber without significant loss in signal integrity. These fibers progressively benefit from reduction of cost and improvement of performance. Meanwhile, the electronic interfaces used in an optical network are not capable of exploiting the ultimate bandwidth of the fiber, limiting the throughput of the network. Different solutions at both the system and the circuit levels have been proposed to increase the data rate of the backbone. System-level solutions are based on the utilization of wave-division multiplexing (WDM), using different colors of light to transmit several sequences simultaneously. In parallel with that, a great deal of effort has been put into increasing the operating rate of the electronic transceivers using highly-developed fabrication processes and novel circuit techniques.

"The increasing demand for high-speed transport of data has revitalized optical communications, leading to extensive work on high-speed device and circuit design. This book deals with the design of high-speed integrated circuits for optical communication transceivers. Building upon a detailed understanding of optical devices, the book describes the analysis and design of critical building blocks, such as transimpedance and limiting amplifiers, laser drivers, phase-locked loops, oscillators, clock and data recovery

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circuits, and multiplexers. This second edition of this best selling textbook has been updated to provide information on the latest developments in the field"--

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