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Driving the development of high-speed networks and 5G, photonic integrated circuits (PICs) are a well-known technology in the telecom world – mainly thanks to the frantic development of transceivers and passive components that are smaller, faster, cheaper and greener than their bulk-optics counterparts.

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Meeting the Challenges of Photonic Integrated Circuit (PIC) testing for next-generation networks Three industry innovators – EXFO, Hewlett Packard Enterprise (HPE) and MPI Corporation – have combined forces to address the challenges posed by optical component testing with faster and more reliable measurement techniques.

~~Photonic integrated circuits | PIC testing challenges | EXFO~~

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Often compared to the groundbreaking electronic processor, photonic integrated circuits (PIC) are similarly imprinted onto wafers before being cut into individual chips. The PIC can combine many functions: purely optical as well as opto-electronical, electro-optical or fully electronic, hence offering the best of optics and electronics.

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